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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10083552	FILING DATE 02/27/2002	CLASS 327	SUBCLASS 536	GAU 2816	EXAMINER
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**APPLICANT: Nakamura Hiroshi;

**CONTINUING DATA VERIFIED:
THIS APPLICATION IS A DIV OF 09/656,831 09/07/2000

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** FOREIGN APPLICATIONS VERIFIED:
JAPAN 11-254553 09/03/1999

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 001701.00672
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Voltage generating/transferring circuit		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
ISSUE FEE			Total Claims	Print Claim for O.G.	
Amount Due	Date Paid		DRAWING		
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
			Applicant Examiner		
			PREPARED FOR ISSUE		
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